

REMARKS

Claims 1-21 and 23-31 will be pending upon entry of the present amendment. Claims 20 and 23 are currently amended. Claim 22 is being canceled. Claims 1-19 and 24-31 were allowed. No new matter is being added to the application.

The applicants appreciate the indication that claims 22-23 were directed to allowable subject matter. Accordingly, claim 20 is being amended to include the language of claim 22. Claim 20 is not being amended to include the language of claim 21, from which claim 22 depended, because the language of claim 21 is not necessary to distinguish amended claim 20 from the cited prior art. In particular, U.S. Patent Application Publication No. 2002/0138712 to Yoshida does not teach or suggest a control device that intercepts accesses of the core of a VLIW processor to first and second memories connected between the core of the VLIW processor and an instruction-cache memory of the VLIW processor, as recognized by the Examiner. That is true regardless of whether the translation subsystem operates based on a code table stored in a translation memory (claim 21 language). Accordingly, amended claim 20 is in condition for allowance. Claims 21 and 23 depend on claim 20, and thus, are also in condition for allowance.

All of the claims remaining in the application are now clearly allowable. Favorable consideration and a Notice of Allowance are earnestly solicited. The Director is authorized to charge any additional fees due by way of this Amendment, or credit any overpayment, to our Deposit Account No. 19-1090.

Respectfully submitted,
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